

**Six-Pack (Three Phase) SiC MOSFET Module  
100 Amperes / 1700 Volts**

**Description:**

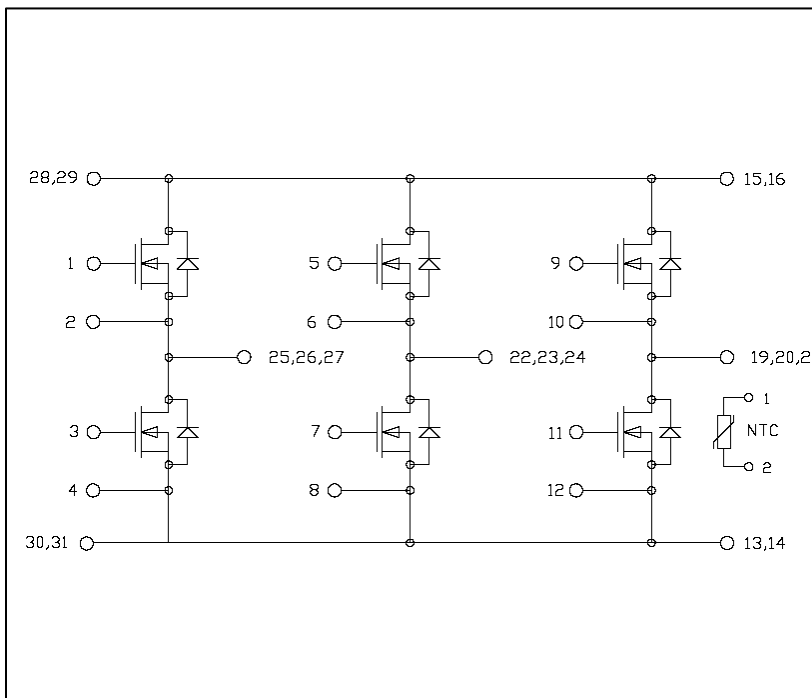
Powerex Silicon Carbide MOSFET Modules are designed for use in high frequency applications. Each module consists of six MOSFET Silicon Carbide Transistors with each transistor having a reverse connected fast recovery free-wheel silicon carbide Schottky diode. All components and interconnects are isolated from the heat sinking baseplate, offering simplified system assembly and thermal management.

**Features:**

- Junction Temperature: 175°C
- Industry Leading  $R_{DS(on)}$
- High Speed Switching
- Low Switching Losses
- Low Capacitance
- Low Drive Requirement
- High Power Density
- Zero Reverse Recovery from Diode
- Isolated Baseplate
- Aluminum Nitride Isolation

**Applications:**

- Energy Saving Power Systems
- High Frequency Type Power Systems
- High Temperature Power Systems



**Absolute Maximum Ratings,  $T_j = 25^\circ\text{C}$  unless otherwise specified**

Characteristics	Symbol	QJE1710SA1	Units
Drain-Source Voltage (G-S Short)	$V_{DSS}$	1200	Volts
Gate-Source Voltage, DC, D-S short	$V_{GSS}$	$\pm 20$	Volts
Drain Current (Continuous) at $T_c=61^\circ\text{C}^{*1}$	$I_D$	100	Amperes
Drain Current (Pulse, Repetitive) $^{*2}$ , $T_{vj}=150^\circ\text{C}^{*3}$	$I_{D(pulse)}$	200	Amperes
Maximum Power Dissipation ( $T_c=25^\circ\text{C}$ , $T_j < 175^\circ\text{C}$ ) $^{*1}$	$P_D$	410	Watts
Maximum Junction Temperature	$T_{jmax}$	175	$^\circ\text{C}$
Operating Junction Temperature, Continuous operation (under switching)	$T_{jop}$	-40 to 150	$^\circ\text{C}$
Maximum Case Temperature $^{*1}$	$T_{cmax}$	125	$^\circ\text{C}$
Storage Temperature	$T_{stg}$	-40 to 125	$^\circ\text{C}$
Mounting Torque, M5 Mounting Screws	—	5	N-m
Module Weight (Typical)	—	180	Grams
Isolation Voltage	$V_{ISO}$	4000	Volts

$^{*1}$  Case temperature ( $T_c$ ) and heat sink temperature ( $T_s$ ) are defined on the each surface (mounting side) of base plate and heat sink under the chips.

$^{*2}$  Pulse width and repetition rate should be such that device junction temperature ( $T_j$ ) does not exceed  $T_{j(MAX)}$  rating.

$^{*3}$  Junction temperature ( $T_{vj}$ ) should not increase beyond  $T_{j(MAX)}$  rating.

**DC Characteristics,  $T_j=25^\circ\text{C}$  unless otherwise specified**

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain Source Leakage Current	$I_{DSS}$	$V_{DS}=1700\text{V}$ , $V_{GS}=0\text{V}$	-	-	1.0	mA
Gate Source Leakage Current	$I_{GSS}$	$V_{DS}=0\text{V}$ , $V_{GS}=15\text{V}$	-	-	0.5	$\mu\text{A}$
Gate Source Threshold Voltage	$V_{GS(th)}$	$V_{DS}=10\text{V}$ , $I_D=37.5\text{mA}$	1.8	-	3.2	Volts
Drain Source On-Resistance (chip)	$R_{DS(on)}$	$V_{GS}=15\text{V}$ , $I_D=100\text{A}$	-	14.5	22.5	mΩ
		$T_j=150^\circ\text{C}$	-	22	-	mΩ
Internal Gate Source Series Resistance	$R_g$	Per Switch	-	1.5	-	Ω
Stray Inductance	$L_s$	P-N	-	10	-	nH

**Dynamic Characteristics, T<sub>J</sub>=25°C unless otherwise specified**

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Input Capacitance	C <sub>ISS</sub>		-	9.2	-	nF
Output Capacitance	C <sub>OSS</sub>	V <sub>GS</sub> =0V, V <sub>DS</sub> =10V	-	5.7	-	nF
Reverse Transfer Capacitance	C <sub>RSS</sub>		-	1.3	-	nF
Turn-On Delay Time	t <sub>D(on)</sub>	V <sub>DD</sub> =900V, V <sub>GS</sub> = ±15V	-	200	-	ns
Rise Time	t <sub>r</sub>	I <sub>D</sub> =120A, R <sub>G</sub> =1Ω, T <sub>J</sub> =150°C	-	50	-	ns
Turn-Off Delay Time	t <sub>D(off)</sub>	Inductive Load, per Pulse	-	220	-	ns
Fall Time	t <sub>f</sub>		-	30	-	ns
Turn-On Energy	E <sub>on</sub>	V <sub>DD</sub> =900V, V <sub>GS</sub> = ±15V	-	5.4	-	mJ
Turn-Off Energy	E <sub>off</sub>	I <sub>D</sub> =100A, R <sub>G</sub> =1Ω, T <sub>J</sub> =150°C Inductive Load, per Pulse	-	1.7	-	mJ
Total Gate Charge	Q <sub>G</sub>	V <sub>DD</sub> =900V, V <sub>GS</sub> =0 to 15V I <sub>D</sub> =120A, T <sub>J</sub> =25°C	-	267	-	nC

**Anti-parallel Diode, T<sub>J</sub>=25°C unless otherwise specified**

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> =-15V I <sub>S</sub> =100A	-	1.64	-	V
		T <sub>J</sub> =150°C	-	2.52	-	V

**Thermal Resistance Characteristics**

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Thermal Resistance, Junction to Case	R <sub>th(j-c)</sub>	Per MOSFET	-	-	0.36	°C/W
Thermal Resistance, Junction to Case	R <sub>th(j-c)</sub>	Per Diode	-	-	0.40	°C/W
Contact Thermal Resistance	R <sub>th(c-s)</sub>	Per Module, Thermal Grease Applied	-	0.07	-	°C/W

**NTC Thermistor Part**

Characteristics	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Zero Power Resistance	R <sub>25</sub>	T <sub>C</sub> =25°C	4.85	5.00	5.15	kΩ
Deviation of Resistance	ΔR/R	T <sub>C</sub> =100°C, R <sub>100</sub> =493Ω	-7.3	-	+7.8	%
B constant	B <sub>(25/50)</sub>	B <sub>(25/50)</sub> =ln(R <sub>25</sub> /R <sub>50</sub> ) / (1/T <sub>25</sub> - 1/T <sub>50</sub> ) <sup>-14</sup>	—	3375	—	K
Power Dissipation	P <sub>25</sub>	T <sub>C</sub> =25°C	—	—	10	mW

\*4 R25: Resistance at Absolute Temperature T25 (K), R50: Resistance at Absolute Temperature T50 (K), T25 = 25(°C) + 273.15 = 298.15(K), T50 = 50(°C) + 273.15 = 323.15(K)

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