

1200V/300A Half Bridge SiC MOSFET Module



Description

The PRXS300HF12DFQ1 is a Half Bridge SiC MOSFET Power Module. It integrates high performance SiC MOSFET chips and SiC Diode designed for the applications such as Motor drives and Renewable energy.

Features

- □ Blocking voltage:1200V
- $\Box \quad \mathsf{R}_{\mathsf{DS}(\mathsf{on})} = 6.4 \mathsf{m} \Omega$
- □ Low thermal resistance with Si₃N₄ AMB
- □ 175°C maximum junction temperature
- □ 62mm half bridge module
- Low Switching Losses

Applications

- □ Motor Drives
- □ Vehicle Fast Chargers
- □ Renewable energy
- □ UPS

Circuit Diagram

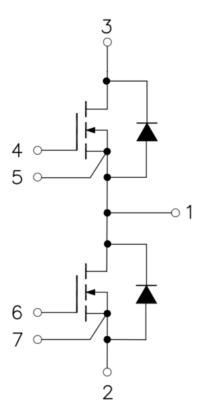


Figure 1. Out drawing & circuit diagram for PRXS300HF12DFQ1



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Pin Configuration and Marking Information

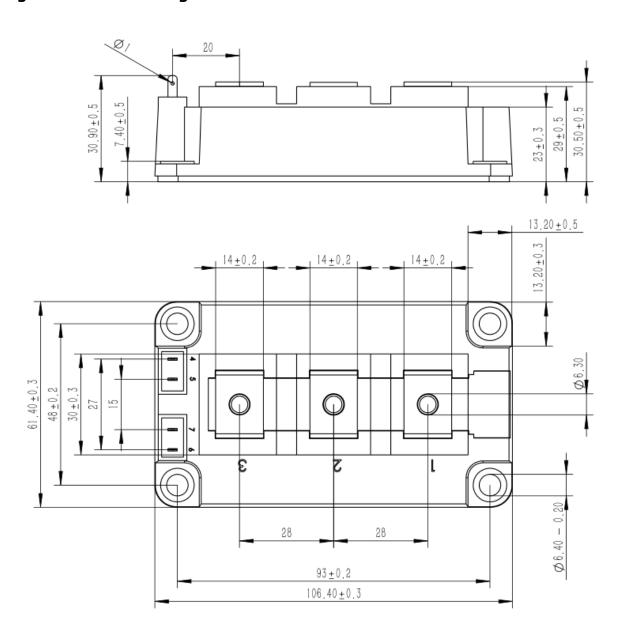


Figure 2. Pin configuration



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Module

Parameter	Conditions	Value	Unit
Isolation Voltage	RMS, f =50Hz, t =1min	4.0	kV
Material of module baseplate	-	Cu	-
Creepage distance	terminal to heatsink terminal to terminal	14.5 10	mm
Clearance	terminal to heatsink terminal to terminal	12.5 10	mm
СТІ	-	>400	-
Module lead resistance, terminals – chip	T _C =25°C	0.6	mΩ
Mounting torque for module mounting	M6	4 to 6	Nm
Weight	-	320	g

Maximum Ratings $(T_j = 25^{\circ}\text{C unless otherwise specified})$

Symbol	Parameter	Condition	Ratings	Unit
V_{DSS}	Drain-Source Voltage	G-S Short	1200	V
$V_{\rm DS\ nom}$	Continuous Operating DC Voltage	Not include surge voltage	1100	V
V _{GSS}	Gate-Source Voltage	D-S Short, AC frequency ≥1Hz, Note1	-10 to 25	V
I_{DS}	DC Continuous Drain Current	$T_C=25$ °C, $V_{GS}=20$ V	360	A
I_{DS}	DC Continuous Drain Current	$T_C = 80^{\circ}C, V_{GS} = 20V$	280	A
I_{DP}	Drain Pulse Current, Peak	T _C =25°C, Less than 1ms, Note2	600	A
I_{F}	Forward Current	T _C =25°C, with ON signal	350	A
I _F	Forward Current	T _C =80°C, with ON signal	260	A
I _{FRM}	Pulse Forward Current	T _C =25°C, Less than 1ms, Note2	600	A
P _{tot}	Total Power Dissipation	T _C =25°C	1666	W
T _{jmax}	Max Junction Temperature	-	175	°C
T_{stg}	Storage Temperature	-	-40 to 125	°C

Note1: Recommended Operating Value, +20V/-5V, +18V/-5V, +15V/-4V

Note2: Pulse width limited by maximum junction temperature



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MOSFET Electrical characteristics (T_j =25°C unless otherwise specified, chip)

	Item	~	Condition		Value		
Symbol		Condit			Тур.	Max	Unit
V _{(BR)DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =3mA		1200	-	-	V
I _{DSS}	Zero gate voltage drain Current	$V_{DS} = 1200V, V_{GS} = 0V$		-	3	-	μΑ
17	C. d. 1 HW.	7 (0 1 11 11	T _j =25°C	1.8	2.5	4.0	V
$V_{GS(th)}$	Gate-source threshold Voltage	$I_D=60\text{mA}, V_{DS}=V_{GS}$	T _j =175°C	-	1.6	-	V
I_{GSS}	Gate-Source Leakage Current	$V_{GS} = 20V, V_{DS} = 0V$	T _j =25°C	-	60	600	nA
		I _D =300A	$T_j = 25$ °C	-	6.4	-	mΩ
R _{DS(on)}	Static drain-source	V _{GS} =20V	T _j =175°C	-	12.3	-	mΩ
(Chip)	On-state resistance	I _D =300A	T _j =25°C	-	7.2	-	mΩ
		$V_{GS}=18V$	T _j =175°C	-	12.9	-	mΩ
		I _D =300A	T _j =25°C	-	1.9	-	V
V _{DS(on)}	Static drain-source	$V_{GS}=20V$	T _j =175°C	-	3.7	-	V
(Chip)		I _D =300A	T _j =25°C	-	2.2	-	V
		$V_{GS}=18V$	T _j =175°C	-	3.9	-	V
Ciss	Input Capacitance	V _D =800V, V _{GS} =0V, f=100kHz, V _{AC} =25mV		-	19.0	-	nF
Coss	Output Capacitance			-	1.08	-	nF
C _{rss}	Reverse transfer Capacitance			-	0.11	-	nF
Q _G	Total gate charge	V _{DD} =800V, I _D =150A, V _{GS}	V _{DD} =800V, I _D =150A, V _{GS} =+20/-5V		660	-	nC
R _{Gint}	Internal Gate Resistance	T _j =25°C		-	2.0	-	Ω
,			T _j =25°C	-	42	-	
$t_{d(on)}$	Turn-on delay time		T _j =150°C	-	38	-	ns
	Chip) On-state resistance Chip) On-state Voltage Chip) On-state Voltage Ciss Input Capacitance Coss Output Capacitance Crss Reverse transfer Capacitance QG Total gate charge Internal Gate Resistance Turn-on delay time Rise time		T _j =25°C	-	36	-	
t_r	Rise time	V _{DD} =600V	T _j =150°C	-	32	-	ns
		I _D =300A	T _j =25°C	-	32	-	
$t_{ m d(off)}$		T _j =150°C	-	43	-	ns	
		T _j =25°C	-	17	-		
t_{f}			T _j =150°C	-	21	-	ns
E	Turn-on power dissipation	Inductive load switching	T _j =25°C	-	5.6	-	- mJ
Eon		operation	T _j =150°C	-	7.9	-	
Е	T	7	T _j =25°C	-	1.6	-	mJ
$E_{ m off}$	Turn-off power dissipation		T _j =150°C	-	2.8	-	
R _{th(j-c)}	FET Thermal Resistance	Junction to Case		-	0.09	-	°C/W



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SiC SBD Electrical characteristics $(T_j = 25^{\circ}\text{C unless otherwise specified, chip})$

		Condition		Value			
Symbol	Item			Min.	Тур.	Max	Unit
V-	Di-d-E		T _j =25°C	-	1.90	-	V
V _F Dio	Diode Forward Voltage	$I_F = 300A, V_{GS} = -4V$	T _j =175°C	-	3.35	-	V
4	D' 1 D D T'		$T_j = 25$ °C	-	25	-	
t _{rr} Diode R	Diode Reverse Recovery Time	V _{RR} =600V, I _D =300A	T _j =150°C	-	27	-	ns
Ţ	D. I. C	MOSFET side:	T _j =25°C	-	115	-	
I _{RM} Peak reverse recovery Current	Peak reverse recovery Current	$V_{GS} = +18/-5V$	T _j =150°C	-	140	-	A
0	Decreed shares	$R_{G(on)} = R_{G(off)} = 3.3\Omega$	T _j =25°C	-	1.9	-	C
Q _{rr} Recovere	Recovered charge	Inductive load	T _j =150°C	-	2.6	-	μС
Err	D	switching operation	T _j =25°C	-	0.5	-	I
\mathbf{E}_{IT}	Reverse recovered energy		T _j =150°C	-	1.3	-	mJ
R _{th(j-c)}	Thermal Resistance, Junction to Case (Diode)		-	0.12	-	°C/W	

Test Conditions

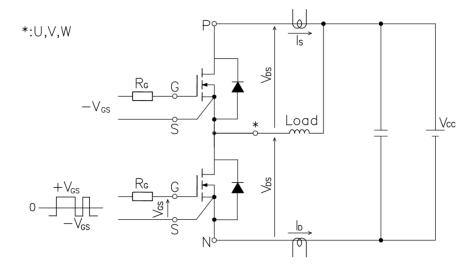


Figure 3. Switching time measure circuit



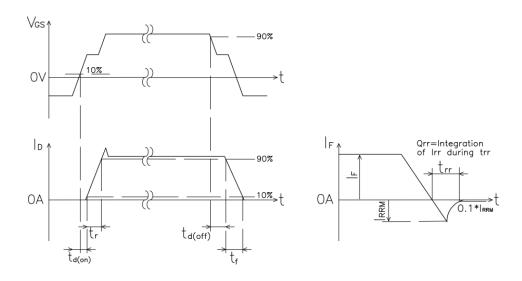


Figure 4. Switching time definition

