

04/24 Rev 0

# PRXS17SU12Z7C1

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# 1200V/17mΩ Single Unit SiC MOSFET Module



### Description

The PRXS17SU12Z7C1 is a Single Unit SiC MOSFET Power Module. It integrates high performance SiC MOSFET chips designed for the applications such as Motor drives and EV charging.

#### **Features**

- $\hfill\square$  1200V/17m $\Omega$  @T\_j = 25°C, V\_{GS} = 18V
- □ 175°C maximum junction temperature
- □ Low Device Capacitances (Coss,Crss)
- □ Faster and More Efficient Switching

### Applications

- Motor Drive
- □ EV Charging
- □ High Voltage DC-DC Converters
- Induction Heating and Welding
- Smart Grid Transmission and Distribution





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### **Pin Configuration and Marking Information**



Figure 2. Pin configuration



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### Module

Parameter	Conditions	Value	Unit
Isolation Voltage	RMS, f =50Hz, t =1min	3.4	KV
СТІ	-	>400	-
Mounting torque for module mounting	M4	1.5	Nm
Weight	-	28	g

# **Maximum Ratings** ( $T_j = 25$ °C unless otherwise specified)

Symbol	Parameter	Conditions	Ratings	Unit	
V <sub>DSS</sub>	Drain-Source Voltage	G-S Short	1200	V	
V <sub>GSS</sub>	Gate-Sourse Voltage	D-S Short, AC frequency $\geq 1$ Hz, Note1	-10 to +22	V	
IDS	DC Continuous Drain Current	V	Tc=25°C	105	А
IDS	DC Continuous Drain Current	VGS -+15 V	Tc=100°C	75	А
I <sub>DS</sub>	DC Continuous Drain Current	$V_{} = 10V$	Tc=25°C	110	А
I <sub>DS</sub>	DC Continuous Drain Current	V GS -+10 V	T <sub>c</sub> =100°C	80	А
Idsm	Pulse Drain Current	Pulse width $\leq 3\mu s$ , V <sub>GS</sub> =+15V, Note2	313	А	
P <sub>tot</sub>	Total Power Dissipation	$T_C = 25^{\circ}C$	350	W	
T <sub>jmax</sub>	Max Junction Temperature	-	175	°C	
Tstg	Storage Temperature	-	-50 to 175	°C	

Note1: Recommended Operating Value, +18V/-5V, +15V/-4V Note2: Pulse width limited by maximum junction temperature



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## **MOSFET Electrical Characteristics** (T<sub>j</sub> =25°C unless otherwise specified, chip)

	-			Value			<b>T</b> T <b>1</b> /
Symbol	Item	Conditio	Condition		Тур.	Max	Unit
V(BR)DSS	Drain-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =100uA		1200	-	-	V
IDSS	Zero gate voltage drain Current	V <sub>DS</sub> =1200V, V <sub>GS</sub> =0V		-	1	-	μΑ
Vague Cata source t	Cate course threshold Valtage	$I_D=35mA, V_{DS}=V_{GS}$	$T_j = 25^{\circ}C$	1.8	2.7	-	V
V GS(th)	Gate-source threshold voltage		$T_j = 175^{\circ}C$	-	2.05	-	
Igss	Gate-Source Leakage Current	$V_{GS}=22V, V_{DS}=0V$	$T_j = 25^{\circ}C$	-	-	100	nA
		I <sub>D</sub> =60A	$T_j = 25^{\circ}C$	-	20	-	mΩ
R <sub>DS(on)</sub>	Static drain-source On-	$V_{GS}$ =+15V	$T_j = 175^{\circ}C$	-	29	-	mΩ
(Chip)	state resistance	ID=60A	$T_j = 25^{\circ}C$	-	17	22.5	mΩ
		$V_{GS} = +18V$	$T_j = 175^{\circ}C$	-	25	-	mΩ
		I <sub>D</sub> =60A	$T_j = 25^{\circ}C$	-	1.20	-	V
V <sub>DS(on)</sub>	Static drain-source On-	$V_{GS} = +15V$	$T_j = 175^{\circ}C$	-	1.74	-	V
(Chip)	state Voltage	ID=60A	$T_j = 25^{\circ}C$	-	1.02	1.35	V
		$V_{GS} = +18V$	$T_j = 175^{\circ}C$	-	1.50	-	V
Ciss	Input Capacitance	$V_{D}=800V, V_{GS}=0V,$ f=1MHz, V <sub>AC</sub> =25mV V <sub>DD</sub> =800V, I <sub>D</sub> =60A, V <sub>GS</sub> =+15/-5V		-	5814	-	pF
Coss	Output Capacitance			-	177	-	pF
Crss	Reverse transfer Capacitance			-	14.2	-	pF
Qg	Total gate charge			-	180	-	nC
R <sub>Gint</sub>	Internal Gate Resistance	$T_j = 25^{\circ}C$		-	1.3	-	Ω
	Turn-on delay time		$T_j = 25^{\circ}C$	-	45	-	
Ld(on)			$T_j\!=\!\!150^\circ C$	-	39	-	ns
4		-	$T_j = 25^{\circ}C$	-	18	-	
Lr.	Rise time	V <sub>DD</sub> =800V	$T_j = 150^{\circ}C$	-	16	-	ns
		I <sub>D</sub> =60A	$T_j = 25^{\circ}C$	-	20	-	
td(off)	td(off) I urn-off delay time	$V_{GS} = +15/-4V$	$T_j = 150^{\circ}C$	-	25	-	ns
	E.N.C	$R_{gon}=R_{goff}=1.0\Omega$	$T_j = 25^{\circ}C$	-	11	-	ns
tf	Fall time	Inductive load switching	$T_j = 150^{\circ}C$	-	35	-	
F	Turn-on power dissipation	operation	$T_j = 25^{\circ}C$	-	492	-	
Eon			$T_j = 150^{\circ}C$	-	662	-	μ
Г	Turn-off power dissipation	]	$T_j = 25^{\circ}C$	-	205	-	
Loff			$T_j \!=\! 150^\circ C$	-	200	-	μ
Rth(j-c)	FET Thermal Resistance	Junction to Case		-	0.36	0.48	K/W



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## **Body Diode Electrical Characteristics** $(T_j = 25^{\circ}C \text{ unless otherwise specified, chip})$

C	Item	Condition		Value			<b>T</b> T •4
Symbol				Min.	Тур.	Max	Unit
V <sub>SD</sub> I	Body Diode Forward Voltage	$V_{GS} = -5V$	$T_j = 25^{\circ}C$	-	4.6	-	v
		I <sub>SD</sub> =30A	$T_j = 175^{\circ}C$	-	4.2	-	
T <sub>rr</sub> F	Reverse recovery time		$T_j = 25^{\circ}C$	-	35	-	ns
		$V_{DD} = 800V, I_D = 60A$	$T_j = 150^{\circ}C$	-	47	-	
0	D I	$V_{GS} = +15/-4V$	$T_j = 25^{\circ}C$	-	240	-	G
Qrr Reverse recovery charge	$K_{gon} = K_{goff} = 1.022$	$T_j = 150^{\circ}C$	-	475	-	nC	
Irrm Re	Reverse recovery current	Inductive load	$T_j = 25^{\circ}C$	-	14	-	А
		switching operation	$T_j = 150^{\circ}C$	-	21	-	А

## **Test Conditions**



Figure 3. Switching time measure circuit



Figure 4. Switching time definition



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