

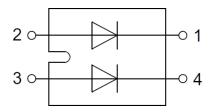
3rd Generation 1200V/100A SiC Schottky Barrier Diode

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PLS100J120A



Circuit diagram



Package Type: SOT-227





Description

The PLS100J120A SiC Schottky Barrier Diode (SBD) Module has been developed using Powerex's advanced 3rd generation SiC SBD technology with the highest performance and reliability. It registers higher efficiency, higher operation temperature and lower loss and can be operated at higher frequency than Si-based solutions. As to the Schottky structure, it shows no recovery at turn-off and allows a low leakage current with reverse voltage up to 1200V. It can contribute to system miniaturization and achieve lightweight system design. Using RoHS compliant components, it is qualified for use in industrial application.

Features

- □ Zero Reverse Recovery Current
- ☐ Ceramic Package Provides 2.5kV Isolation
- □ Positive temperature coefficient
- □ Temperature-independent performance
- ☐ High-speed switching
- □ Low switching loss
- □ Low heat dissipation requirements
- □ RoHS compliant

Applications

- □ Solar inverter
- ☐ Uninterruptible Power Supply (UPS)
- □ Switched-mode power supplies
- Welding equipment
- □ High speed rectifier

Product Specifications

Device	V _{RRM}	I _F (110°C)	V _F (25°C)	Qc	Marking
PLS100J120A	1200V	117A	1.40V	539nC	PLS100J120A



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Maximum Ratings $(T_c = 25^{\circ}C, \text{ unless otherwise specified})$

Parameter	Symbol	Value	Unit	Test conditions
Repetitive peak reverse voltage	V_{RRM}	1200		T _C = 25°C
Surge peak reverse voltage	V _{RSM}	1200	V	$T_C = 25^{\circ}C$
DC reverse voltage	V _{DC}	1200		$T_C = 25^{\circ}C$
		195		$T_C = 25^{\circ}C$
Continuous forward current	I _F	117	А	T _C = 110°C
		100		T _C = 128°C
		000		$T_C = 25$ °C, $t_p = 10$ ms, half
Surge non-repetitive forward current	I _{FSM}	800	Α	sine pulse
Power dissipation	P _{tot}	535	W	T _C = 25°C
i ² t value	∫i²dt	3200	A ² s	$T_C = 25^{\circ}\text{C}, t_p = 10\text{ms}$
Virtual junction temperature	T _{VJ}	-40~175	°C	
Operation temperature	T _{OP}	-40~150	°C	
Storage temperature	T _{stg}	-40~150	°C	
Mounting torque	М	1.1	Nm	M4 screw

Thermal Resistance

Downwater	Complete	Values			11!1	Test
Parameter	Symbol	Min.	Тур.	Max.	Unit	condition
Thermal resistance from junction to case	R _{th(j-c)}	/	0.28	/	°C/W	



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Static Electrical Characteristics $(T_j = 25^{\circ}C, \text{ unless otherwise specified})$

5		Values				
Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions
DC blocking voltage	V _{DC}	1200	/	/	V	I _R = 200 μA
	V _F	/	1.40	1.60	V	I _F = 100A, T _j = 25°C
Forward voltage		/	1.90	2.30		I _F = 100A, T _j = 175°C
_	I _R	/	20	300	μA	V _R = 1200V, T _j = 25°C
Reverse current		/	100	1600		V _R = 1200V, T _j = 175°C

Dynamic Electrical Characteristics (T_j = 25°C, unless otherwise specified)

Danamatan	Symbol	Values			11	-
Parameter		Min.	Тур.	Max.	Unit	Test conditions
		/	7821	/	pF	$V_R = 0V, f = 1MHz$
Total capacitance	С	/	503	/		V _R = 400V, f = 1MHz
		/	375	/		V _R = 800V, f = 1MHz
Total capacitive charge	Q _C	/	539	/	nC	V _R = 800V
Capacitance stored energy	Ec	/	154	/	μJ	V _R = 800V



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Electrical Characteristic Diagrams

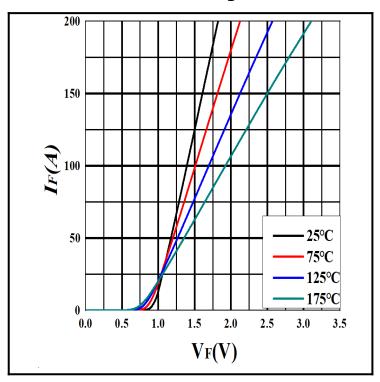
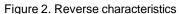
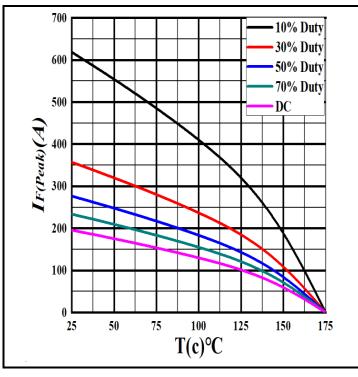


Figure 1. Forward characteristics



8000



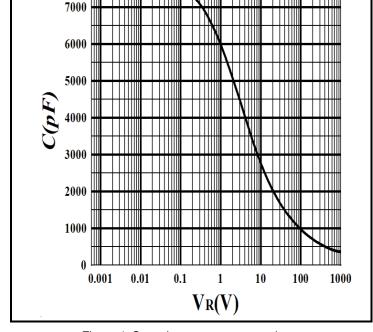
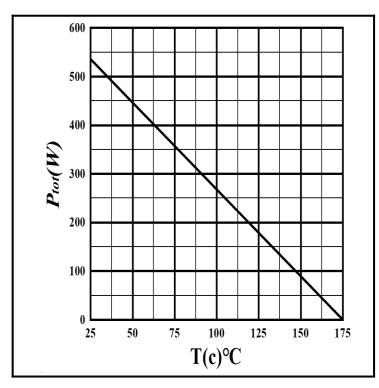


Figure 3. Current derating

Figure 4. Capacitance vs. reverse voltage



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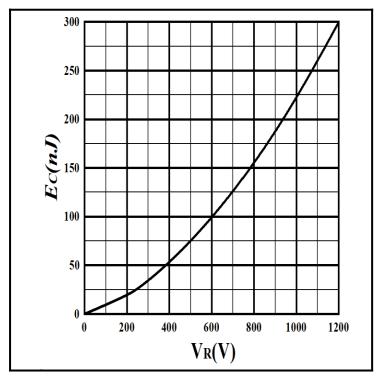


Figure 5. Power derating



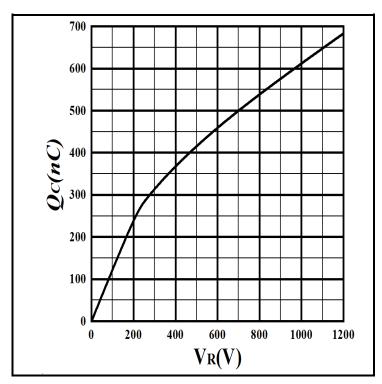


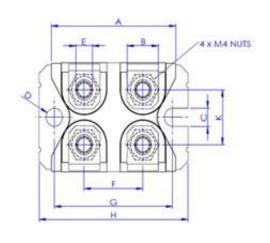
Figure 7. Total capacitance charge vs. reverse voltage

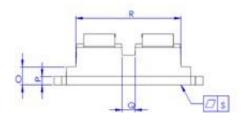


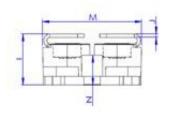
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Package Information







Dimension unit: [mm]							
Symbol	Min	Nom	Max				
A	31.50	31.80	32.10				
В	7.70	8.00	8.30				
C	4.10	4.20	4.30				
D	4.10	4.20	4.30				
E	4.10	4.24	4.30				
F	14.90	15.00	15.15				
G	29.80	30.20	30.50				
Н	37.80	38.00	38.30				
I	11.70	11.82	12.20				
J	0.75	0.80	0.85				
K	12.50	12.75	13.00				
M	25.00	25.75	25.50				
N	6.70	6.90	7.05				
О	4.10	4.20	4.50				
P	1.90	2.00	2.10				
Q	3.20	3.36	3.60				
R	26.60	26.78	27.00				
S	-0.03	0.05	0.10				



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