

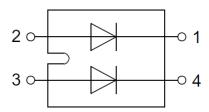
3rd Generation 650V/100A SiC Schottky Barrier Diode

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PLS100J065A



Circuit diagram



Package Type: SOT-227





Description

The PLS100J065A SiC Schottky Barrier Diode (SBD) Module has been developed using Powerex's advanced 3rd generation SiC SBD technology with the highest performance and reliability. It registers higher efficiency, higher operation temperature and lower loss and can be operated at higher frequency than Si-based solutions. As to the Schottky structure, it shows no recovery at turn-off and allows a low leakage current with reverse voltage up to 650V. It can contribute to system miniaturization and achieve lightweight system design. Using RoHS compliant components, it is qualified for use in industrial application.

Features

- □ Zero Reverse Recovery Current
- ☐ Ceramic Package Provides 2.5kV Isolation
- □ Positive temperature coefficient
- □ Temperature-independent performance
- □ High-speed switching
- □ Low switching loss
- □ Low heat dissipation requirements
- □ RoHS compliant

Applications

- □ Solar inverter
- ☐ Uninterruptible Power Supply (UPS)
- ☐ Switched-mode power supplies
- □ Welding equipment
- □ High speed rectifier

Product Specifications

Device	V _{RRM}	I _F (110°C)	V _F (25°C)	Qc	Marking
PLS100J065A	650V	103A	1.45V	275nC	PLS100J065A



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Maximum Ratings $(T_c = 25^{\circ}C, \text{ unless otherwise specified})$

Parameter	Symbol	Value	Unit	Test conditions
Repetitive peak reverse voltage	V_{RRM}	650		T _C = 25°C
Surge peak reverse voltage	V_{RSM}	650	V	$T_C = 25^{\circ}C$
DC reverse voltage	V _{DC}	650		$T_C = 25^{\circ}C$
		164		$T_C = 25^{\circ}C$
Continuous forward current	IF	103	А	T _C = 110°C
		100		T _C = 114°C
Surge non-repetitive forward current	I _{FSM}	700	А	$T_C = 25$ °C, $t_p = 10$ ms, half
Carge non repeative forward current	IFSIVI			sine pulse
Power dissipation	P _{tot}	394	W	T _C = 25°C
i ² t value	∫i²dt	2450	A ² s	$T_C = 25$ °C, $t_p = 10$ ms
Virtual junction temperature	T _V J	-40~175	°C	
Operation temperature	T _{OP}	-40~150	°C	
Storage temperature	T _{stg}	-40~150	°C	
Mounting torque	М	1.1	Nm	M4 screw

Thermal Resistance

Donomoston	Coursels al	Values			1.1	Test	
Parameter	Symbol	Min.	Тур.	Max.	Unit	condition	
Thermal resistance from junction to case	R _{th(j-c)}	/	0.38	/	°C/W		



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Static Electrical Characteristics $(T_j = 25$ °C, unless otherwise specified)

D	Symbol	Values			114	
Parameter		Min.	Тур.	Max.	Unit	Test conditions
DC blocking voltage	V_{DC}	650	/	/	V	I _R = 200 μA
		/	1.45	1.70	٧	$I_F = 100A, T_j = 25^{\circ}C$
Forward voltage	V _F	/	1.70	2.30		I _F = 100A, T _j = 175°C
	I _R	/	10	200	μA	$V_R = 650V, T_j = 25^{\circ}C$
Reverse current		/	60	1200		V _R = 650V, T _j = 175°C

Dynamic Electrical Characteristics ($T_j = 25$ °C, unless otherwise specified)

Barranatan	Symbol	Values			11	
Parameter		Min.	Тур.	Max.	Unit	Test conditions
		/	5089	/		$V_R = 0V, f = 1MHz$
Total capacitance	С	/	520	/	pF	V _R = 200V, f = 1MHz
		/	443	/		V _R = 400V, f = 1MHz
Total capacitive charge	Q _C	/	275	/	nC	V _R = 400V
Capacitance stored energy	Ec	/	41	/	μJ	V _R = 400V



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Electrical Characteristic Diagrams

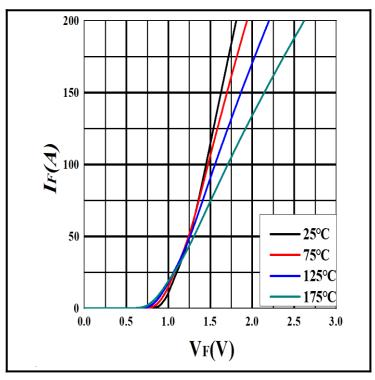
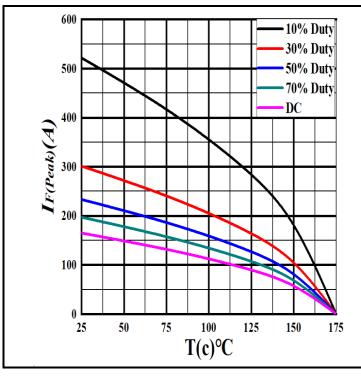


Figure 1. Forward characteristics

Figure 2. Reverse characteristics



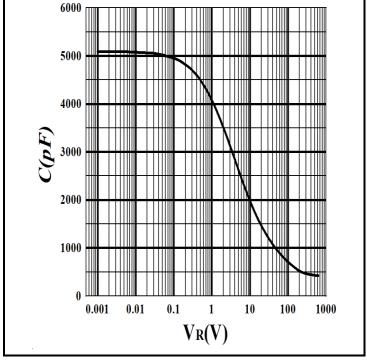
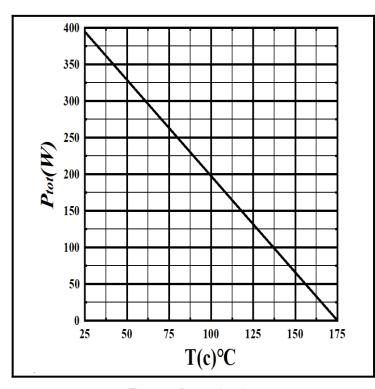


Figure 3. Current derating

Figure 4. Capacitance vs. reverse voltage



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80 80 40 20 100 200 300 400 500 600 VR(V)

Figure 5. Power derating

Figure 6. Capacitance stored energy

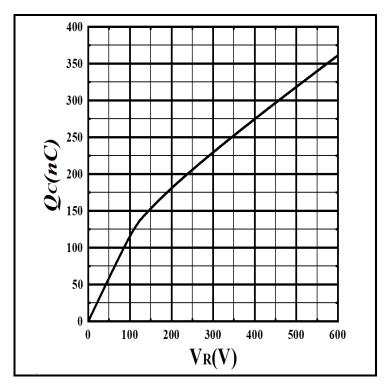
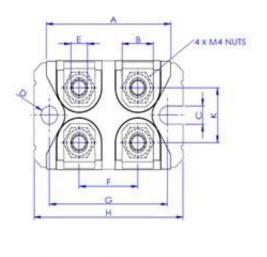


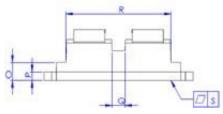
Figure 7. Total capacitance charge vs. reverse voltage

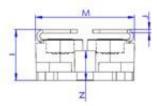


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Package Information







Dimension unit: [mm]							
Symbol	Min	Nom	Max				
A	31.50	31.80	32.10				
В	7.70	8.00	8.30				
C	4.10	4.20	4.30				
D	4.10	4.20	4.30				
E	4.10	4.24	4.30				
F	14.90	15.00	15.15				
G	29.80	30.20	30.50				
Н	37.80	38.00	38.30				
I	11.70	11.82	12.20				
J	0.75	0.80	0.85				
K	12.50	12.75	13.00				
M	25.00	25.75	25.50				
N	6.70	6.90	7.05				
О	4.10	4.20	4.50				
P	1.90	2.00	2.10				
Q	3.20	3.36	3.60				
R	26.60	26.78	27.00				
S	-0.03	0.05	0.10				



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