



PDS025J170H5

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5th Generation 1700V/25A SiC Schottky Barrier Diode

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PDS025J170H5 Circuit diagram PIN 1 CASE PIN 2 CASE Package Type: TO-247-2L

Description

The PDS025J170H5 SiC Schottky Barrier Diode (SBD) has been developed using Powerex's advanced 5th generation SiC SBD technology with the highest performance and reliability. It registers higher efficiency, higher operation temperature and lower loss and can be operated at higher frequency than Si-based solutions. As to the Schottky structure, it shows no recovery at turn-off and allows a low leakage current with reverse voltage up to 1700V. It can contribute to system miniaturization and achieve lightweight system design. Using RoHS compliant components, it is qualified for use in industrial application.

Features

- Revolutionary semiconductor material Silicon Carbide (SiC)
- □ No reverse recovery
- □ High-speed switching performance
- □ Temperature-independent switching behavior
- System cost / size savings due to reduced cooling requirements
- □ Junction temperature range from -55°C to 175°C
- □ RoHS compliant

Applications

- ☐ Industrial power supplies: Industrial UPS
- □ Battery chargers
- Solar inverters
- ☐ Switch mode power supplies

Product Specifications

Device	V _{RRM}	I _F (135°C)	V _F (25°C)	Qc	Marking
PDS025J170H5	1700V	40A	1.40V	191nC	PDS025J170H5



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Maximum Ratings $(T_c = 25^{\circ}C, \text{ unless otherwise specified})$

Parameter	Symbol	Value	Unit	Test conditions
Repetitive peak reverse voltage	V_{RRM}	1700		T _C = 25°C
Surge peak reverse voltage	V _{RSM}	1700	V	T _C = 25°C
DC reverse voltage	V _{DC}	1700		T _C = 25°C
		87		T _C = 25°C
Continuous forward current	l _F	39	Α	T _C = 135°C
		25		T _C = 155°C
Surge non-repetitive forward current	I _{FSM}	388	А	T_C = 25°C, t_p = 10ms, half sine pulse
Surge repetitive forward current	I _{FRM}	171	А	T_C = 25°C, t_p = 10ms, half sine wave D = 0.1
Power dissipation	P _{tot}	428	W	T _C = 25°C
i ² t value	∫i²dt	752	A ² s	$T_C = 25^{\circ}C, t_p = 10 ms$
Operating junction temperature	Tj	-55~175	°C	
Storage temperature	T _{stg}	-55~175	°C	
Mounting torque	М	1	Nm	M3 screw

Thermal Resistance

Davamatan	Symbol	Values			11	Test
Parameter		Min.	Тур.	Max.	Unit	condition
Thermal resistance from junction to case	R _{th(j-c)}	/	0.35	/	°C/W	



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Static Electrical Characteristics ($T_j = 25$ °C, unless otherwise specified)

Barrantan		Values			11	
Parameter	Symbol	Min.	Тур.	Max.	Unit	Test conditions
DC blocking voltage	V _{DC}	1700	/	/	V	I _R = 100 μA
		/	1.40	1.60	٧	$I_F = 25A, T_j = 25^{\circ}C$
Forward voltage	V _F	/	2.15	2.70		I _F = 25A, T _j = 175°C
	I _R	/	3	75	μA	V _R = 1700V, T _j = 25°C
Reverse current		/	30	400		V _R = 1700V, T _j = 175°C

Dynamic Electrical Characteristics ($T_j = 25$ °C, unless otherwise specified)

Barrantan	Symbol	Values			11	
Parameter		Min.	Тур.	Max.	Unit	Test conditions
	C	/	2943	/	pF	$V_R = 0V, f = 1MHz$
Total capacitance		/	116	/		V _R = 600V, f = 1MHz
		/	90	/		V _R = 1200V, f = 1MHz
Total capacitive charge	Qc	/	191	/	nC	V _R = 1200V
Capacitance stored energy	Ec	/	80	/	μJ	V _R = 1200V



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Electrical Characteristic Diagrams

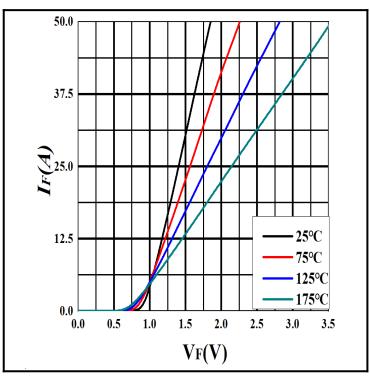


Figure 1. Forward characteristics

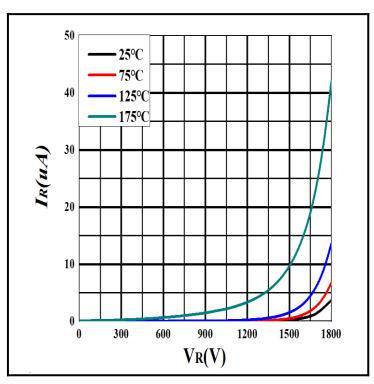
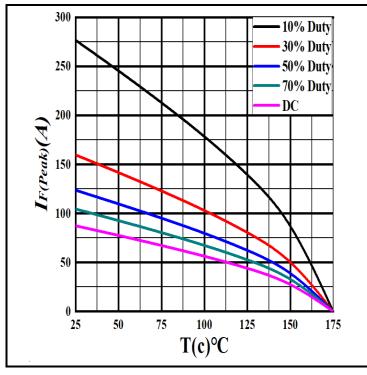


Figure 2. Reverse characteristics





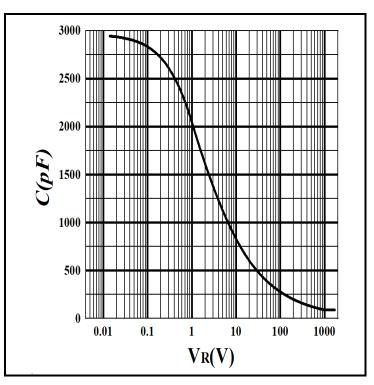
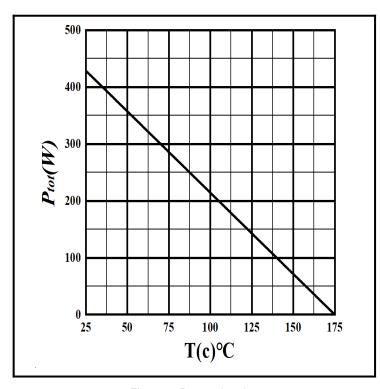


Figure 4. Capacitance vs. reverse voltage



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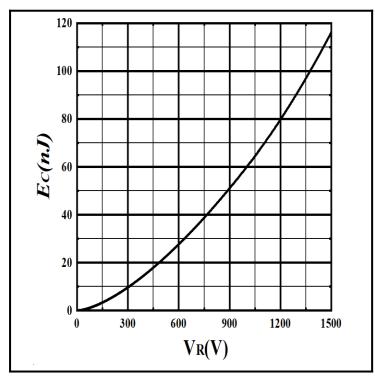


Figure 5. Power derating



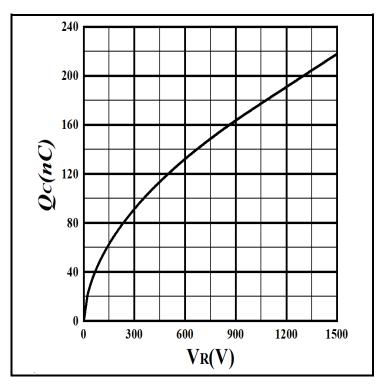


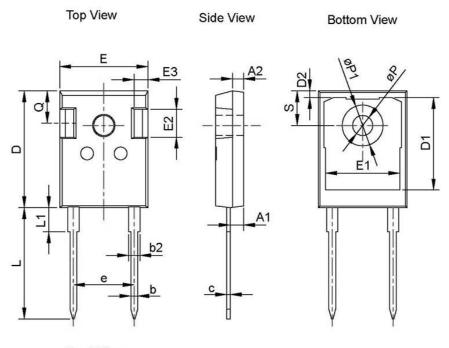
Figure 7. Total capacitance charge vs. reverse voltage

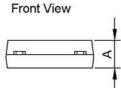


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Package Information





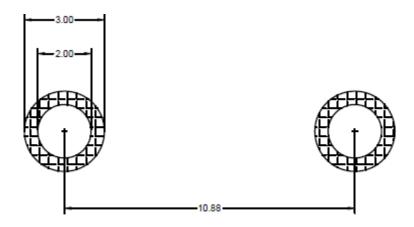
Dimension unit: [mm]							
Symbol	Min	Nom	Max				
Α	4.80	5.00	5.20				
A1	2.21	2.41	2.61				
A2	1.85	2.00	2.15				
b	1.11	1.21	1.36				
b2	1.91	2.01	2.21				
С	0.51	0.60	0.75				
D	20.70	21.00	21.30				
D1	16.25	16.55	16.85				
D2	1.00	1.20	1.35				
Е	15.50	15.80	16.10				
E1	13.00	13.30	13.60				
E2	4.80	5.00	5.20				
E3	2.30	2.50	2.70				
е	10.88 BSC						
L	19.62	19.92	20.22				
L1	-	-	4.30				
øΡ	3.40	3.60	3.80				
øP1	-	-	7.30				
Q	5.40	5.80	6.20				
S	6.20 BSC						



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Recommended Solder Pad Layout



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Note: All dimensions are in mm